Į.
_
۵
$\Box$
α
$\subseteq$
Ν
0
٥
ij.
$\exists$
0
Ŧ
₹
>
>
≥
`
$\geq$
a
7
-
4

		STUDY MODULE D	ESCRIPTION FORM			
	the module/subject	and computing science in		Code 1010324391010324814		
Field of study			Profile of study (general academic, practic (brak)	Year /Semester 5 / 9		
Electrical Engineering  Elective path/specialty  Electrical and Computer Systems in			Subject offered in: Polish	Course (compulsory, elective)  obligatory		
Cycle of	study:		Form of study (full-time,part-tim	e)		
First-cycle studies			part-time			
No. of h	_			No. of credits		
Lectur	Claddo		Project/seminars:	- 2		
Status o		program (Basic, major, other)	(university-wide, from anothe			
Education	on areas and fields of sci	(brak)		(brak)  ECTS distribution (number		
Luucaii	on areas and helds of sci	ence and air		and %)		
techr	ical sciences			2 100%		
Technical sciences				2 100%		
Responsible for subject / lecturer:  Dr inż. Jerzy Frąckowiak email: jerzy.frackowiak@put.poznan.pl tel. 616652382						
	tryczny					
ul. F	iotrowo 3A, 60-965 P	oznań				
Prerequisites in terms of knowledge, skills and social competencies:						
1	Knowledge	Basic knowledge of automation,	control theory, and microcon	ntrollers.		
2	Skills	The ability to understand and int	sterpret the messages conveyed and effective self.			
3	Social competencies	Awareness of the need to broaden their competence.				
Assu	mptions and obj	ectives of the course:				
Synthesis of selected industrial control systems, development of control programs for PLCs, their start-up and testing.						
	Study outco	mes and reference to the	educational results for	or a field of study		
Know	/ledge:			-		
architecture, instruction set, timers, counters, interrupts PLC Siemens S7-1200 - [K_W07+]     selected PLC programming languages - [K_W07+]						
Skills		<u> </u>				
use the knowledge gained to create algoritms control and write application programs - [K_U04+]     selected PLC programming languages - [K_U04+]						
	I competencies:	<del> </del>				

# Assessment methods of study outcomes Lecture: - Final test. Laboratory: - The development of the control algorithm, the design and the control sample run control system.

# Faculty of Electrical Engineering

## **Course description**

PLCs, their architecture, interrupts, timers, PLC programming languages??, algorithms, sample control industrial systems, the SFC diagrams and control programs.

# Basic bibliography:

- 1. Mikulczyński T., Samsonowicz Z.: "Automatyzacja dyskretnych procesów produkcyjnych", WNT, Warszawa 1997.
- 2. Seta Z.: "Wprowadzenie do zagadnień sterowania&, Wydawnictwo Mikom, Warszawa 2002.
- 3. Kamiński K.: "Programowanie w Step 7 Microwin", GRYF, Warszawa 2006.
- 4. Dokumentacja sterownika S7-1200 firmy Siemens.

### Additional bibliography:

1. Bubnicki Z.: "Teoria i algorytmy sterowania", Wydawnictwo Naukowe PWN, Warszawa 2002

# Result of average student's workload

Activity	Time (working hours)
1. participation in lectures	9
2. consultations for lectures	3
3. credit lecture	2
4. participation in laboratory classes	9
5. preparation of projects	4
6. consultation for laboratory classes	5
7. preparation for the completion of lectures	6
8. preparation laboratory	6

### Student's workload

Source of workload	hours	ECTS
Total workload	44	2
Contact hours	28	1
Practical activities	24	1